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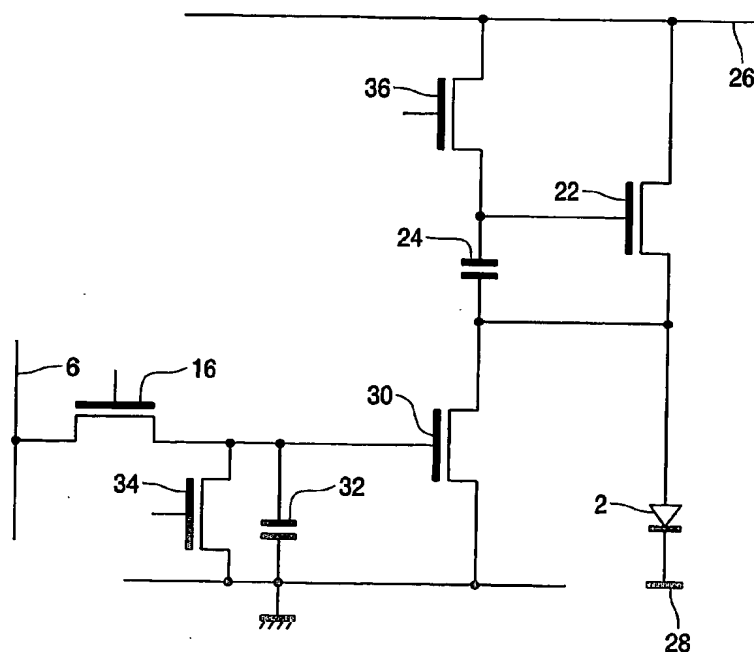
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- (71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **FISH, David, A.** [GB/GB]; c/o Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB). **HECTOR, Jason, R.** [GB/GB]; c/o Philips Intellectual Property
- (74) Agent: **WILLIAMSON, Paul, L.**; c/o Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB).
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[Continued on next page]

(54) Title: ACTIVE MATRIX DISPLAY DEVICE



(57) Abstract: An addressing scheme is provided for a display having an array of current driven pixels. An input voltage is used for generating a desired source-drain current using a transistor operated with low duty cycle. This source-drain current is then passed through a drive transistor and the resulting gate-source voltage is stored on a capacitor for subsequent driving of the display element.



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